PCT / IB 03 / 02218 #2
Rec'd PCT/PTO 07 DEC 2004
21. 05. 03

Europäisches Patentamt

European Patent Office

Office européen des brevets

IB03 /2218

REC'D 16 JUN 2003
WIPO PCT

Bescheinigung

Certificate

Attestation

Die angehefteten Unterlagen stimmen mit der ursprünglich eingereichten Fassung der auf dem nächsten Blatt bezeichneten europäischen Patentanmeldung überein.

The attached documents are exact copies of the European patent application described on the following page, as originally filed.

Les documents fixés à cette attestation sont conformes à la version initialement déposée de la demande de brevet européen spécifiée à la page suivante.

Patentanmeldung Nr. **Patent application No.** **Demande de brevet n°**

02077294.3

PRIORITY DOCUMENT

SUBMITTED OR TRANSMITTED IN COMPLIANCE WITH RULE 17.1(a) OR (b)

Der Präsident des Europäischen Patentamts;
Im Auftrag

For the President of the European Patent Office

Le Président de l'Office européen des brevets
p.o.

R C van Dijk

Anmeldung Nr:
Application no.: 02077294.3
Demande no:

Anmeldetag:
Date of filing: 12.06.02
Date de dépôt:

Anmelder/Applicant(s)/Demandeur(s):

Koninklijke Philips Electronics N.V.
Groenewoudseweg 1
5621 BA Eindhoven
PAYS-BAS

Bezeichnung der Erfindung/Title of the invention/Titre de l'invention:
(Falls die Bezeichnung der Erfindung nicht angegeben ist, siehe Beschreibung.
If no title is shown please refer to the description.
Si aucun titre n'est indiqué se referer à la description.)

Image display system having an analog display for displaying matrix signals

In Anspruch genommene Prioriät(en) / Priority(ies) claimed /Priorité(s) revendiquée(s)
Staat/Tag/Aktenzeichen/State/Date/File no./Pays/Date/Numéro de dépôt:

Internationale Patentklassifikation/International Patent Classification/
Classification internationale des brevets:

G09G5/00

Am Anmeldetag benannte Vertragstaaten/Contracting states designated at date of filing/Etats contractants désignées lors du dépôt:

AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU MC NL PT SE TR

Image display system having an analog display for displaying matrix signals

The invention relates to an image display system comprising an analog display for displaying first, second and third matrix information signals, which are suitable to be displayed on a matrix display device having a plurality of pixels each pixel having a plurality of color pixel sub-components, and have a time period allocated for transferring information related to one pixel.

A system of rendering matrix information signals with pixel sub-component precision on a matrix display including a plurality of pixels is known from WO 01/09873 A1. When applying the matrix information signals with pixel sub-component precision to an analog display like a cathode ray tube, the rendering of the information on the display screen of the analog display is deteriorated compared to when signals are applied without pixel sub-component precision.

It is an object of the invention to provide a system capable of rendering of matrix information signals on the display screen with pixel sub-component precision. The invention is defined by the independent claims. The dependent claims define advantageous embodiments.

If for example matrix information signals, adapted for displaying information with pixel sub- component precision on a matrix display device having a repetitive sub-pixel sequence RGB, represent a white pixel which is shifted over a length of one sub-component, then one of the matrix signals, in this example the first signal, is shifted in time with an amount TP. The result on the matrix display is that instead of the pixel sequence RGB, now the sequence GBR' will light up as a white dot, so a shift of one pixel sub-component has been realized. When applying these matrix information signals directly to an analog device the information of the first signal would be shifted on the screen by an amount V*TP with respect to the information of the second and third signal, resulting in a deteriorated rendering of the information. However when applying the shifting of V*TP/N to the matrix information signals applied to the analog display, the white dot on the screen of the analog display would also be shifted by an amount V*TP/N, because the first, second and third information signal

are on an average shifted by V*TP/N. Moreover the shifting makes the first, second and third signals for generating the white dot substantially coinciding, so that discoloration is avoided. In one of the embodiments a more detailed explanation is provided.

When applying pixel sub-component precision, images are rendered with more detail and sharpness. When the images contain text, the readability of the text is improved. The matrix displays can have three sub-components, but also versions exist which have more sub-components. In the latter case more different colored sub components could be present or a sub-component of a particular color could appear more than once in the sequence.

It is a further advantage if the processing elements comprise time shifting means adapted for shifting in time the first, respectively the third information signal with an amount of approximately –TP/3, respectively +TP/3 with respect to the second information signal. Using time shifting means is a cost effective solution, as components and circuitry can be applied as used in a conventional system.

It is also a further advantage if the processing means comprise convergence driving means for delivering a convergence correction signal; and a line convergence adjuster for shifting upon receipt of the convergence correction signal the rendering on the screen of:

the first information signal with an amount of substantially V*TP/3 with respect to the second information signal in the counter-scanning direction, and

the third information signal with an amount of substantially V*TP/3 with respect to the second information signal in the scanning direction.

The shifting is now realizable by means of the line convergence adjuster. The basic function of the adjuster in a conventional system comprising an analog display is the adjusting of the convergence. The same adjuster can be used for realizing the shifting on the screen by supplying one convergence correction signal to the adjuster. In this embodiment it is generally not necessary to adapt the matrix information signals. Also this is a cost-effective solution of realizing the shifting.

The display system may be a combination of units like a combination of a computer device and a monitor or a combination of video processing equipment and a monitor or a television. The system may also be comprised in one housing or unit like a monitor, television or in a display combined with circuit units.

It is a further advantage if the processing means comprise:

a line convergence adjuster for shifting the rendering on the screen of the first, respectively the third information signal with respect to the second information signal in the scanning direction; and

convergence driving means apt for delivering a convergence correction signal to the line convergence adjuster for shifting the rendering on the screen of the first and the third information signal with an amount of substantially -V*TP/3, respectively +V*TP/3 with respect to the second information signal.

Making use of the convergence adjuster results in a cost-effective realization of the shifting of the rendering of the first and third information signal.

It is a further advantage if the line convergence adjuster comprises a magnetic quadrupole able to receive a current as the correction signal from the driving means. This quadrupole which is used to adapt the line convergence can also be used effectively to realize the shifting. When applying the current, which is a DC-current, having a maximum value at the center of the screen and reducing towards the edges of the screen, the desired shifting is obtained.

It is also advantageous if a switch is present for switching ON/ OFF the current. When receiving matrix information signals adapted for rendering the information with pixel sub-component precision the current can be switched ON for rendering the matrix information signals with pixel sub-component precision on the analog display. When other signals which are not adapted for rendering information with sub-component precision, are received the current can be switched OFF, thereby allowing correct rendering of the other signals. The switch can be a mechanical switch or an electronic switch, controlled via a user interface or via an automatic detection of the matrix information signals with sub-component precision. The user interface can comprise a button on the device or a remote control device.

These and other aspects of the invention will be further elucidated and described with reference to the drawings, in which:

Fig. 1 shows a schematic diagram of a matrix display device, not according to the invention, having a plurality of pixels;

Figs. 2, 3 and 4 show matrix information signals;

Fig. 5 shows a first embodiment of the display system according to the invention;

Figs. 6, 7 and 8 show output signals displayed with sub-component precision on an analog display screen;

Fig. 9 shows a second embodiment of a display system according to the invention;

Fig. 10 shows the correction signal of the second embodiment;

Fig. 11 shows how the convergence is shifted on the screen;

Fig. 12 shows how to add a switch in the second embodiment.

With the help of Figs. 1 to 4 first of all a simplified explanation will be given of how information can be displayed with sub-component precision on a matrix display device and how these signals are displayed on an analog display. Thereafter various embodiments will be presented.

In Fig. 1a, b and c two adjacent pixels of the known matrix display, like an LCD display, are shown. The pixels are composed of a first trio R1, G1, B1 and a second trio R2, G2, B2 of color sub-components. The two pixels are located next to each other in a row of pixels. Without applying pixel sub-component precision a white dot can either be created on a location P1, by driving the first trio R1, G1, B1 or at a next location P2 in the same row, by driving the second trio R2, G2, B2 as shown in Fig.1a. However when applying matrix information signals adapted for displaying information with pixel sub-component precision to the matrix display, a white dot can also be created on a location P1G as shown in Fig. 1b. The location P1G is shifted by one sub-component in row direction as compared to the location P1. This is achieved by driving the color sub-components G1, B1, R2. Likewise as shown in Fig. 1c a white dot can be created on location P1B by driving color sub-components B1, R2, G2. So, by applying matrix information signals adapted for rendering information with pixel sub-component precision to a matrix display, the resolution, or in other words the amount of detail, in row direction can be increased.

When the matrix information comprises text, then the readability of the text on the screen can be improved. In such a case the image is often composed of black letters on a white background. The features that can be rendered with sub-component precision are amongst others a width of a "leg" of a symbol, the distance between two legs of a symbol, or the distance between two symbols.

Figs. 2, 3 and 4 show the matrix information signals VR, VG, VG adapted for displaying information with sub-component precision on a matrix display as shown in Fig. 1. Fig. 2 shows the matrix information signals VR, VG, VB as function of time t for creating a white spot on the location P1. The voltage level corresponding to no light output is indicated with a level 0, while the level corresponding to maximum light output is indicated with a level 1. When the matrix information signals VR, VG, VB all have the level 1 during a time

period TP1, associated with driving the color sub-components R1, G1, B1, then on position P1 a white dot is displayed. Likewise a level 0 of the matrix information signals during a time period TP2, associated with driving the sub-components R2, G2, B2, means that on position P2 a black dot is displayed. If for example a white dot should be displayed on position P1G, then the signals VR, VG, VB according to Fig. 3 should be supplied. As shown in Fig. 3 the first matrix information signal VR is delayed with a time period TP1 compared to the situation in Fig. 2. The effect is that sub-components G1, B1, R2 will light up, resulting in a white dot on the location P1G as shown in Fig. 1. So, the white dot on the matrix display has been shifted with an amount corresponding to one sub-component or in other words white dots are positioned with sub-component precision on a row of a matrix display. Likewise Fig. 4 shows the matrix information signals VR, VG, VB for displaying a white spot on the location P1B as shown in Fig. 1. Above simplified explanation illustrates how according to the prior art matrix information signals are adapted for displaying information with sub-component precision on a matrix display.

When applying the information signals VR, VG, VB as shown in Fig. 2 to an analog display 3 like a cathode ray tube a white spot would be displayed correctly on the screen of the analog display 3 on a position corresponding with time period TP1.

However, when applying the information signals VR, VG, VB as shown in Fig. 3 to an analog display 3, then on the position corresponding to time TP1 a dot having green and blue sub-components would be displayed and next to this dot a red dot would be displayed on a position shifted over a distance V*TP1 compared to the green-blue dot, wherein V is the scanning velocity in the scanning direction of the analog display 3. So the matrix information suitable to be displayed with pixel sub-component precision on a matrix display, is not rendered correctly on an analog display 3. Likewise the information signal VR, VG, VB as shown in Fig. 4 will not be rendered correctly on the analog display 3. A blue dot will be visible on the position corresponding to time TP1 and next to this dot a red-green dot will be visible.

In a display system according to the first embodiment as shown in Fig. 5 processing elements 2 are present receiving the matrix information signals VR, VG, VB as shown in the Figs. 2, 3 and 4 and the analog display 3 having a display screen 4. The signals VR, VG, VB have a time period TP allocated for transferring information related to one pixel. The processing elements 2 comprise a first delay circuit 11 for delaying the second information signal VG and a second delay circuit 12 for delaying the third information signal VB. The first information signal VR is not delayed by the processing elements 2. The output

signals VR', VG', VB' of the processing elements 2, being the delayed matrix information signals VR, VG, VB, are coupled to the analog display 3, which can be a cathode ray tube. The analog display 3 has a display screen 4 for displaying images composed of substantially parallel lines extending in a scanning direction. The display 3 is able to scan the lines in a scanning direction with a scanning velocity V. The delay time of the first delay circuit 11 is TP/3. The delay time of the second delay circuit 12 is 2*TP/3. When taking the timing of the second information signal VG as reference, the effect is that the first information signal VR is advanced in time with an amount of TP/3, while the third information signal VB is delayed in time with an amount of TP/3 with respect to the second information signal VG. When applying the information signals shown in Figs. 2 to 4 to the above described processing elements 2the resulting output signals VR', VG', VB' will have timing diagrams as shown in Figs. 6 to 8. Fig. 6 shows the output signals VR', VG', VB' resulting from input signals according Fig. 2. Fig. 7 shows the output signals VR', VG', VB' resulting from input signals according Fig. 3. Fig. 8 shows the output signals VR', VG', VB' resulting from input signals according Fig. 4. As can be seen from Figs. 6 to 8 the center C of the output signals VR', VG', VB' is shifted in the situation of Fig. 7 with an amount TP/3 with respect to the situation in Fig. 6 and in the situation of Fig. 8 with an amount 2*TP/3. When displaying the output signals VR', VG', VB' according to Figs. 6 to 8 on the screen 4, the resulting white spot is either not shifted (situation of Fig. 6), is shifted with an amount V*TP/3 in the situation of Fig. 7 or is shifted with an amount 2*V*TP/3 in the situation of Fig. 8. So, the spots are rendered with pixel sub-component precision on positions corresponding to the positions P1, P1G and P1B as shown in Fig. 1.

The invention, illustrated in above embodiment with three color sub-components, can be generalized for a situation where a plurality N of color sub-components are present. In that case the matrix information signals VR, VG, VB comprise N signals, each of which (except one "center signal") has to be shifted in time with an amount proportional to TP/N with respect to the center signal. By applying appropriate time shifts as illustrated above for the example N=3, a white spot can be shifted on the screen 4 with amounts of V*TP/N.

In a second embodiment as shown in Fig. 9 the display system comprises processing elements 2 receiving the matrix information signals VR, VG, VB. The same reference numbers are applied as in the first embodiment for items having a similar function as described in the first embodiment. The processing elements 2 comprise a convergence driving unit 31 and a line convergence adjuster 30. The line convergence adjuster 30 is

mounted on the neck of the analog display 3, having display screen 4. The processing elements 2 deliver the output signals VR', VG', VB' to the analog display 3. As in this second embodiment no special processing of the matrix information signals VR, VG, VB is required compared to a conventional display system, this part of the processing elements 2 will not further be discussed here. The convergence driving unit 31 supplies a convergence correction signal 32 to the line convergence adjuster 30. The line convergence adjuster 30 can be a magnetic quadrupole comprising one or more coils, like is often present in a conventional display system for adjusting the convergence of the analog display 3. By applying a correction signal 32 in the form of a current applied to the coils of the quadrupole 30 as shown in Fig. 10, the convergence is shifted as shown in Fig. 11. The current as function of time t as shown in Fig. 10 is shown for the duration TL required to scan one line. The current has a maximum when the scanning the middle of a line and decreases towards the beginning and end of a line.

The effect, as shown in Fig. 11, is that on the screen 4 of the analog display 3, designed for three primary colors red, green and blue, the red information R is shifted to the left (in the counter-scanning direction) and the blue information B is shifted to the right (in the scanning direction) with respect to the green information G. So, when for example conventional information signals representing three narrow vertical white bars are applied to the display 3, three sets of red, green and blue colored vertical bars RGB are visible as shown in Fig. 11. As can be concluded from this example the correction signal 32 can create the same shift on the screen as the time shift disclosed in the first embodiment.

Fig. 12 shows how in the second embodiment a switch S can be added for switching ON/ OFF the current (being the correction signal 32) depending on whether information signals VR, VG, VB, renderable with sub-component precision, are received. The switch S can be activated by a user via a suitable user interface, but could also be activated via an automatic detection circuit, which detects whether information signals VR, VG, VB renderable with sub-component precision, are present.

It should be noted that the above-mentioned embodiments illustrate rather than limit the invention, and that those skilled in the art will be able to design many alternative embodiments without departing from the scope of the appended claims. In the claims, any reference signs placed between parentheses shall not be construed as limiting the claim. The word "comprising" does not exclude the presence of elements or steps other than those listed in a claim. The word "a" or "an" preceding an element does not exclude the presence of a plurality of such elements. The invention can be implemented by means of hardware

comprising several distinct elements, and by means of a suitably programmed computer. In the device claim enumerating several means, several of these means can be embodied by one and the same item of hardware. The mere fact that certain measures are recited in mutually different dependent claims does not indicate that a combination of these measures cannot be used to advantage.

CLAIMS:

1. An image display system comprising:

an analog display for displaying images composed of substantially parallel lines and having a display screen;

means for scanning the lines in a scanning direction with a scanning velocity V;

processing means for receiving a first, a second and a third matrix information signal suitable to be displayed on a matrix display device having a plurality of pixels, each pixel having a plurality N of colored pixel sub-components and having a time period TP allocated for transferring information related to one pixel to shift the rendering on the screen of at least one of the information signals with respect to another one of the information signals by an amount proportional to V* TP/N.

2. A system as claimed in claim 1, characterized in that each pixel has a predetermined sequence of a red, a green and a blue sub-component; the processing means being arranged for shifting by an amount of substantially V*TP/3 with respect to the second information signal, containing information about the green sub-component, the first information signal, containing information about the red sub-component in the counter-scanning direction; and the third information signal, containing information about the blue sub-component, in the scanning direction.

3. A system as claimed in claim 2, characterized in that the processing elements comprise time shifting means adapted for shifting in time the first, respectively the third information signal with an amount of approximately –TP/3, respectively +TP/3 with respect to the second information signal.

4. A system as claimed in claim 2, characterized in that the processing elements comprise:

convergence driving means for delivering a convergence correction signal; and

a line convergence adjuster for shifting upon receipt of the convergence correction signal the rendering on the screen of:

the first information signal by an amount of substantially V*TP/3 with respect to the second information signal in the counter-scanning direction, and

the third information signal by an amount of substantially V*TP/3 with respect to the second information signal in the scanning direction.

5. A system as claimed in claim 4, characterized in that the line convergence adjuster comprises a magnetic quadrupole able to receive a current as the correction signal from the driving means.

6. A system as claimed in claim 5, characterized in that a switch is present for switching ON/ OFF the current.

ABSTRACT:

Image display system comprising an analog display (3) for displaying images composed of substantially parallel lines and means for scanning the lines in a scanning direction with a scanning velocity (V). The image display device further comprises processing elements (2), which are adapted for receiving a first (VR), a second (VG) and a third (VB) matrix information signal. These information signals (VR,VG,VB) are suitable to be displayed with pixel sub-component precision on a matrix display device having a plurality of pixels. The processing elements (2) are designed for shifting the rendering on the display (3) of at least one of the information signals (VR,VG,VB) with respect to another one of the information signals (VR,VG,VB). As a result of this shifting the information signals (VR,VG,VB) are rendered with sub-component precision on the analog display (3).

Fig. 5

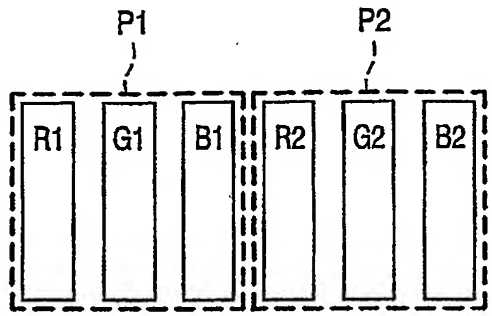

FIG. 1a

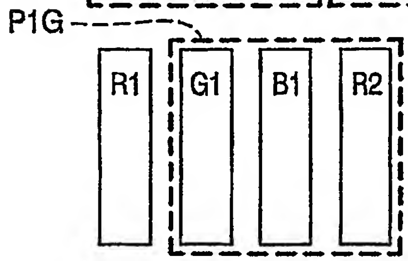

FIG. 1b

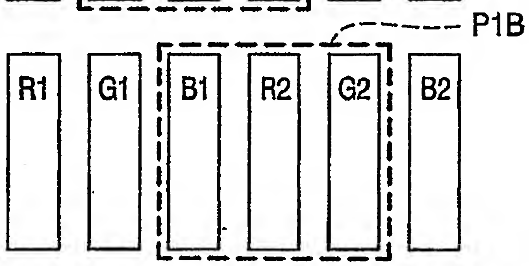

FIG. 1c

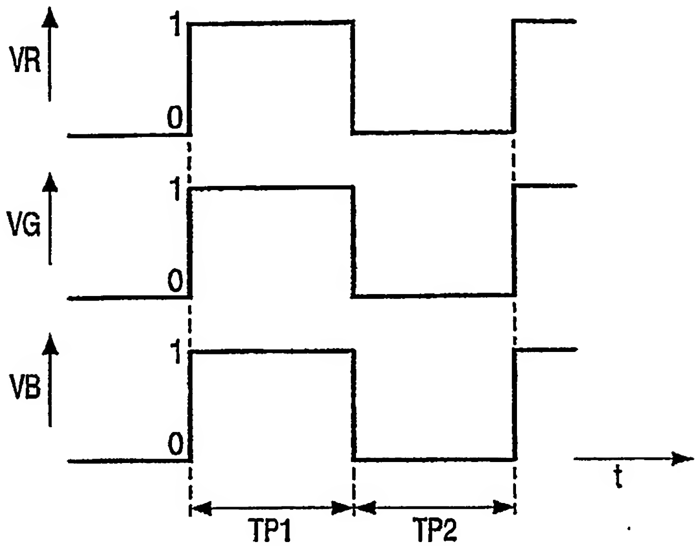

FIG. 2

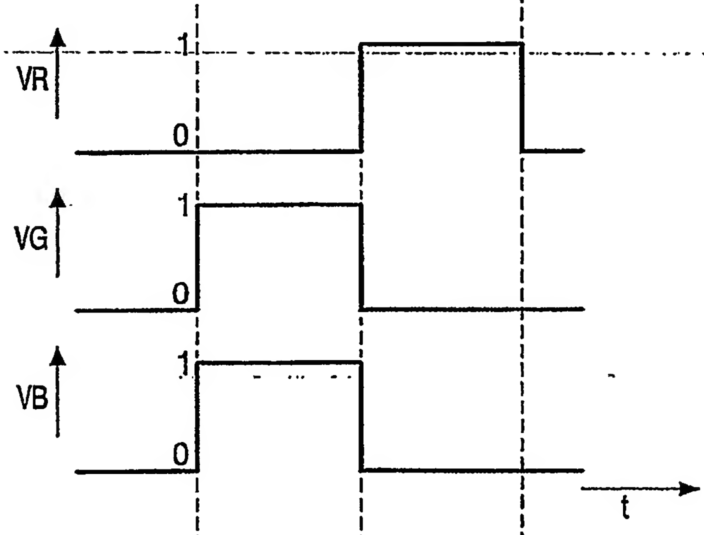

FIG. 3

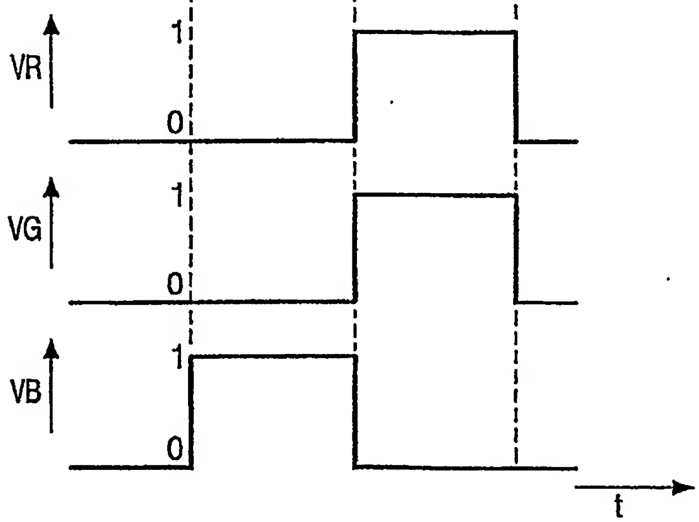

FIG. 4

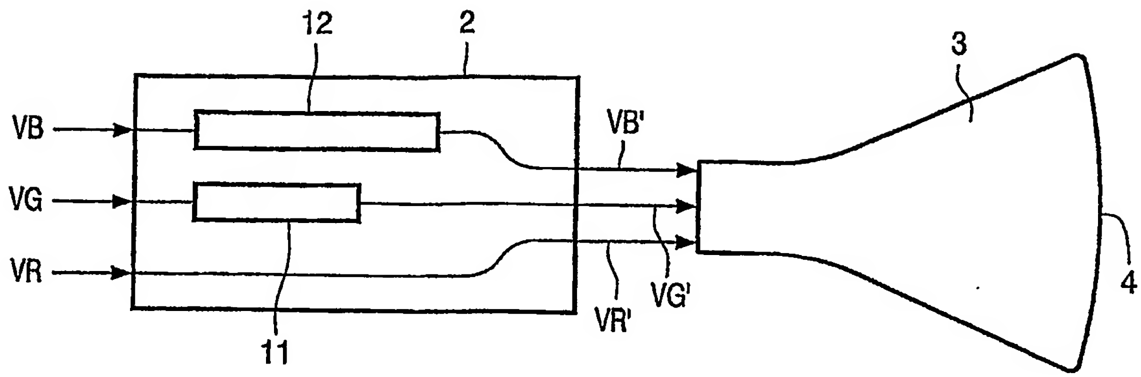

FIG. 5

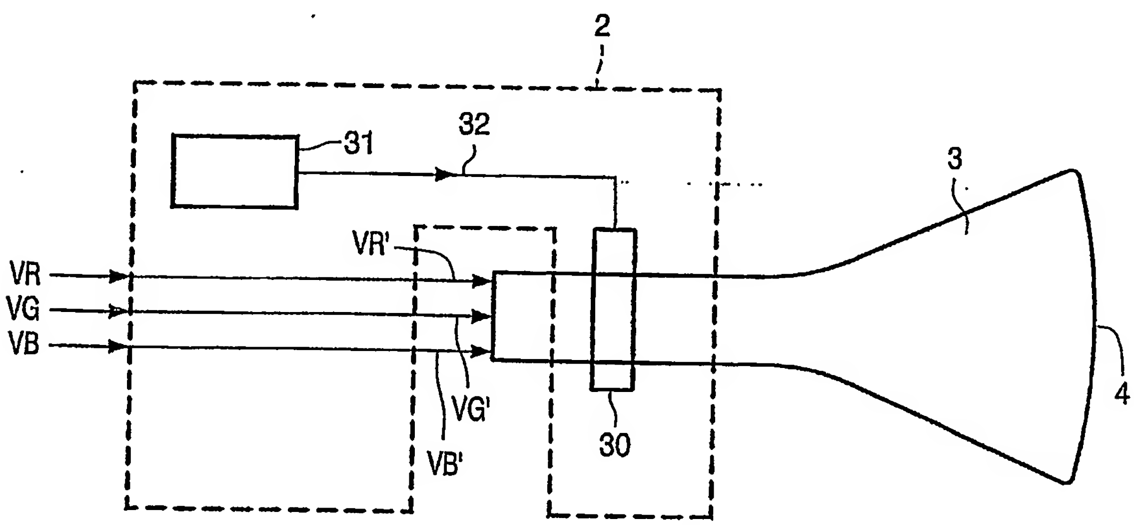

FIG. 9

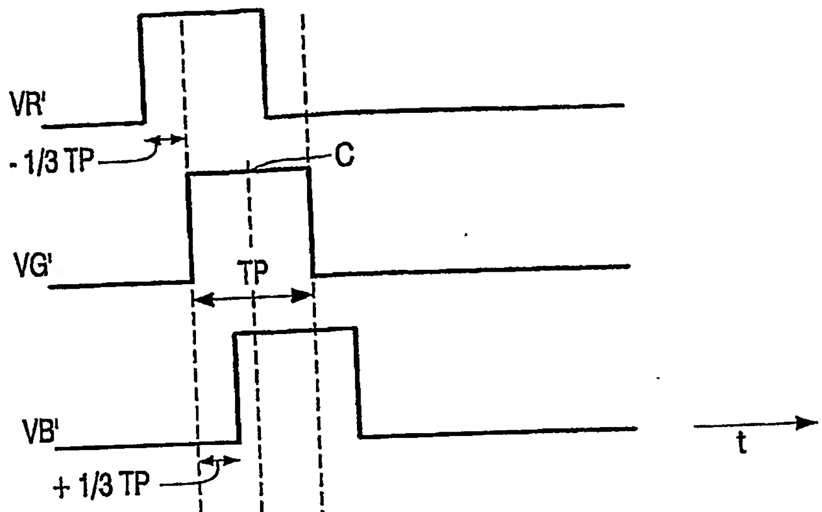

FIG. 6

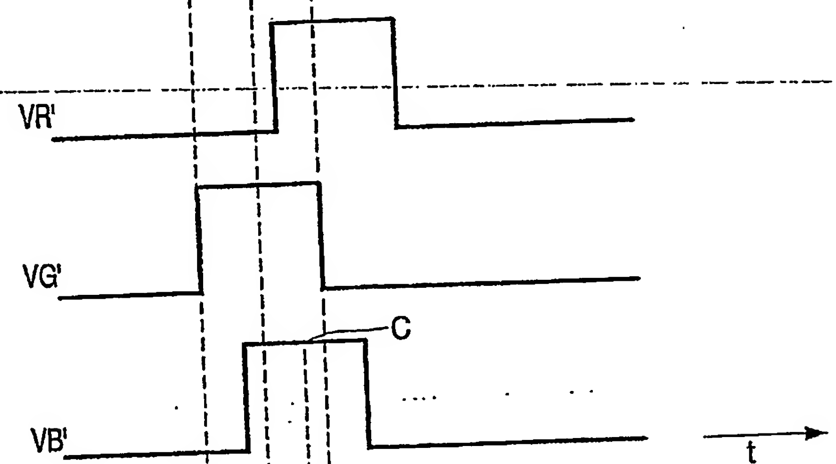

FIG. 7

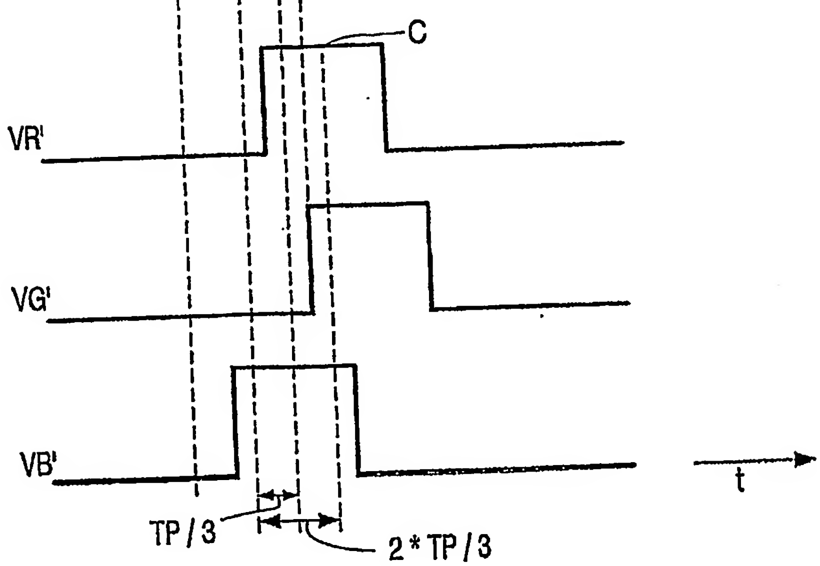

FIG. 8

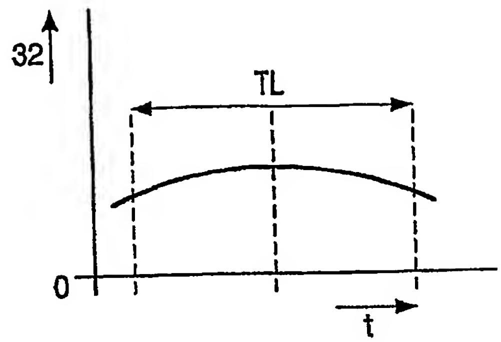

FIG. 10

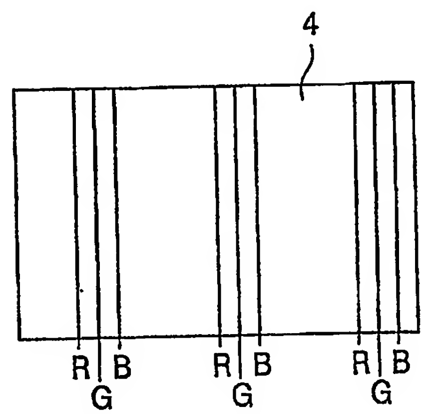

FIG. 11

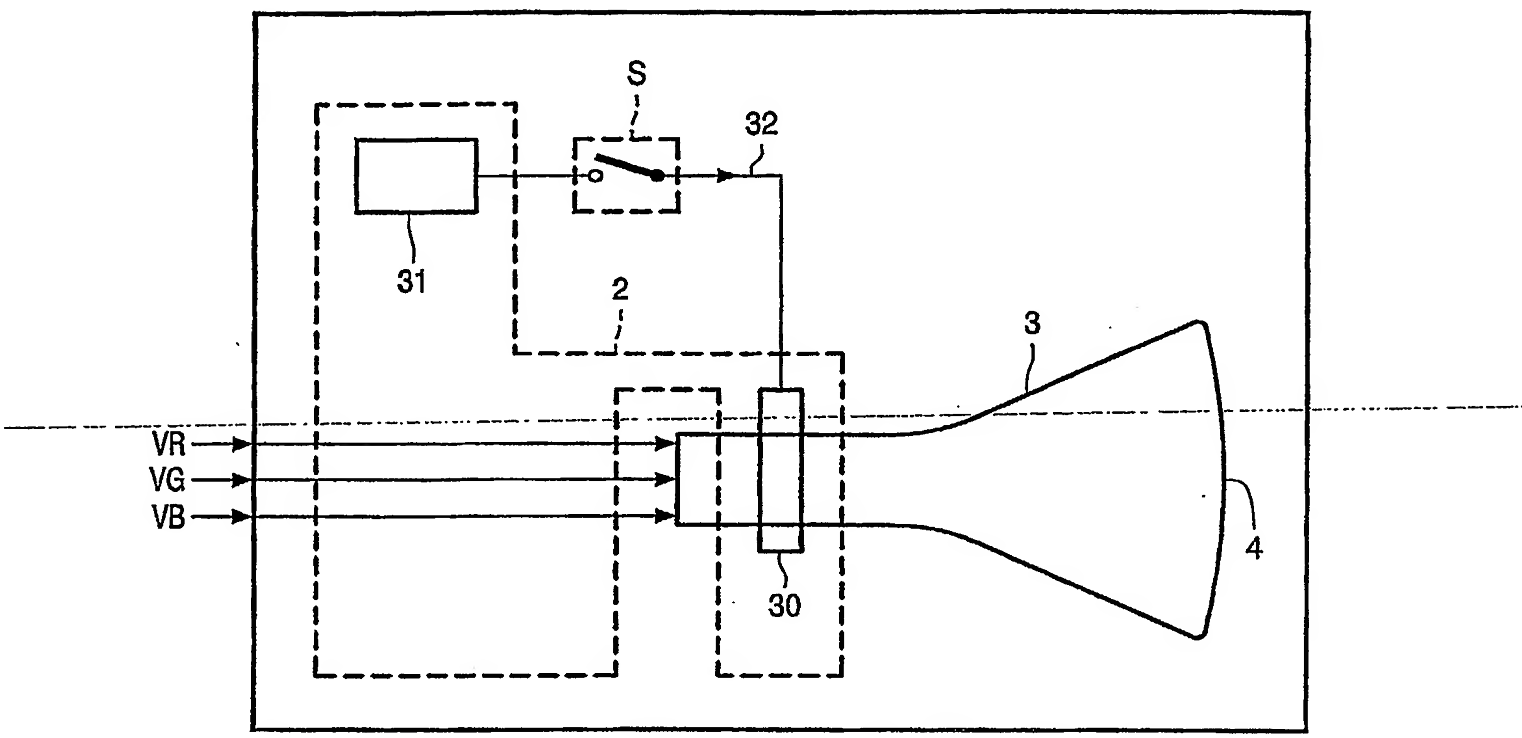

FIG. 12